

**Inventor Name Search Result**

Your Search was:

Last Name = SCHMATZ

First Name = MARTIN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09681625</u>	6425693	150	05/11/2001	OPTICAL FIBER CONNECTOR	SCHMATZ, MARTIN
<u>09682253</u>	Not Issued	071	08/09/2001	SELF ALIGNING OPTICAL DETECTOR	SCHMATZ, MARTIN
<u>09861668</u>	Not Issued	041	05/22/2001	PHASE ROTATOR AND DATA RECOVERY RECEIVER INCORPORATING SAID PHASE ROTATOR	SCHMATZ, MARTIN
<u>10284547</u>	Not Issued	030	10/30/2002	CLOCK DATA RECOVERY SYSTEM	SCHMATZ, MARTIN
<u>10319705</u>	Not Issued	030	12/13/2002	SYSTEM AND METHOD FOR TRANSMITTING DATA AND ADDITIONAL INFORMATION SIMULTANEOUSLY WITHIN A WIRE BASED COMMUNICATION SYSTEM	SCHMATZ, MARTIN
<u>10375286</u>	Not Issued	030	02/27/2003	RECEIVER FOR CLOCK AND DATA RECOVERY AND METHOD FOR CALIBRATING SAMPLING PHASES IN A RECEIVER FOR CLOCK AND DATA RECOVERY	SCHMATZ, MARTIN
<u>10484608</u>	Not Issued	030	01/20/2004	CLOCK DATA RECOVERING SYSTEM WITH EXTERNAL EARLY/LATE INPUT	SCHMATZ, MARTIN
<u>10842804</u>	Not Issued	020	05/11/2004	ADJUSTING PARAMETERS OF A SERIAL LINK	SCHMATZ, MARTIN
<u>10849693</u>	Not Issued	020	05/20/2004	APPARATUS FOR TRANSMITTING AND RECEIVING DATA	SCHMATZ, MARTIN
<u>10014455</u>	Not Issued	030	12/14/2001	DYNAMIC MEASUREMENT OF COMMUNICATION CHANNEL CHARACTERISTICS USING DIRECT SEQUENCE SPREAD SPECTRUM (DSSS) SYSTEMS, METHODS AND PROGRAM PRODUCTS	SCHMATZ, MARTIN L.
<u>10201868</u>	Not Issued	041	07/23/2002	FAST DETECTION OF INCORRECT SAMPLING IN AN OVERSAMPLING CLOCK AND DATA RECOVERY SYSTEM	SCHMATZ, MARTIN L.
<u>10242152</u>	6819813	150	09/11/2002	OPTICAL LAND GRID ARRAY INTERPOSER	SCHMATZ, MARTIN L.

<u>10265759</u>	Not Issued	030	10/07/2002	DIGITAL ADAPTIVE CONTROL LOOP FOR DATA DESERIALIZATION	SCHMATZ, MARTIN L.
<u>10265981</u>	Not Issued	030	10/07/2002	METHOD AND SYSTEM FOR DATA AND EDGE DETECTION WITH CORRELATION TABLES	SCHMATZ, MARTIN L.
<u>10680679</u>	Not Issued	020	10/07/2003	METHOD AND SYSTEM FOR USING STATISTICAL SIGNATURES FOR TESTING HIGH-SPEED CIRCUITS	SCHMATZ, MARTIN L.
<u>10720974</u>	Not Issued	030	11/24/2003	METHOD FOR DETERMINING JITTER OF A SIGNAL IN A SERIAL LINK AND HIGH SPEED SERIAL LINK	SCHMATZ, MARTIN L.
<u>10791175</u>	Not Issued	020	03/02/2004	CIRCUIT AND METHOD FOR PROVIDING AUTOMATIC ADAPTATION TO FREQUENCY OFFSETS IN HIGH SPEED SERIAL LINKS	SCHMATZ, MARTIN L.
<u>10827783</u>	Not Issued	030	04/20/2004	MULTIPLEXER AND DEMULTIPLEXER	SCHMATZ, MARTIN L.
<u>10977667</u>	Not Issued	030	10/29/2004	INTEGRATED LINE DRIVER	SCHMATZ, MARTIN L.
<u>09996053</u>	Not Issued	030	11/28/2001	ANALOG UNIDIRECTIONAL SERIAL LINK ARCHITECTURE	SCHMATZ, MARTIN LEO
<u>09996091</u>	Not Issued	121	11/28/2001	ARCHITECTURE FOR ADVANCED SERIAL LINK BETWEEN TWO CARDS	SCHMATZ, MARTIN LEO
<u>09996113</u>	Not Issued	030	11/28/2001	UNIFIED DIGITAL ARCHITECTURE	SCHMATZ, MARTIN LEO
<u>09997587</u>	Not Issued	041	11/28/2001	APPARATUS AND METHOD FOR OVERSAMPLING WITH EVENLY SPACED SAMPLES	SCHMATZ, MARTIN LEO
<u>10769437</u>	Not Issued	030	01/30/2004	PHASE ROTATOR, PHASE ROTATION METHOD AND CLOCK AND DATA RECOVERY RECEIVER INCORPORATING SAID PHASE ROTATOR	SCHMATZ, MARTIN LEO
<u>10915790</u>	Not Issued	020	08/11/2004	METHODS AND ARRANGEMENTS FOR LINK POWER REDUCTION	SCHMATZ, MARTIN LEO
<u>60262358</u>	Not Issued	159	01/16/2001	GLOBAL ARCHITECTURE FOR ADVANCED SERIAL LINK	SCHMATZ, MARTIN LEO
<u>60262401</u>	Not Issued	159	01/16/2001	UNILINK DIGITAL ARCHITECTURE	SCHMATZ, MARTIN LEO
<u>60262441</u>	Not Issued	159	01/16/2001	UNILINK ANALOG ARCHITECTURE	SCHMATZ, MARTIN LEO
<u>60266365</u>	Not Issued	159	02/02/2001	APPARATUS AND METHOD FOR OVERSAMPLING WITH EVENLY SPACED SAMPLES	SCHMATZ, MARTIN LEO

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**Inventor Name Search Result**

Your Search was:

Last Name = STEVENS

First Name = JOSEPH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09126895</a>	<a href="#">6159055</a>	150	07/31/1998	RF ELECTRODE CONTACT ASSEMBLY FOR A DETACHABLE ELECTROSTATIC CHUCK	STEVENS, JOSEPH
<a href="#">09239687</a>	<a href="#">6304173</a>	150	01/29/1999	REAR VIEW AND MULTI-MEDIA SYSTEM FOR VEHICLES	STEVENS, JOSEPH
<a href="#">09439294</a>	<a href="#">6444101</a>	150	11/12/1999	CONDUCTIVE BIASING MEMBER FOR METAL LAYERING	STEVENS, JOSEPH
<a href="#">09614406</a>	<a href="#">6494219</a>	150	07/12/2000	METHOD AND APPARATUS FOR REMOVAL OF UNWANTED ELECTROPLATING DEPOSITS	STEVENS, JOSEPH
<a href="#">09867780</a>	<a href="#">6635157</a>	150	05/29/2001	ELECTRO-CHEMICAL DEPOSITION SYSTEM	STEVENS, JOSEPH
<a href="#">10269250</a>	Not Issued	161	10/11/2002	METHOD AND APPARATUS FOR REMOVAL OF UNWANTED ELECTROPLATING DEPOSITS	STEVENS, JOSEPH
<a href="#">10690033</a>	Not Issued	030	10/20/2003	ELECTRO-CHEMICAL DEPOSITION SYSTEM	STEVENS, JOSEPH
<a href="#">60191387</a>	Not Issued	159	03/22/2000	METHOD AND APPARATUS FOR REMOVAL OF UNWANTED ELECTROPLATING DEPOSITS	STEVENS, JOSEPH
<a href="#">08444531</a>	<a href="#">5522467</a>	150	05/19/1995	SYSTEM AND APPARATUS FOR INHIBITING HELICAL STACK-OUT	STEVENS, JOSEPH C.
<a href="#">60216204</a>	Not Issued	159	07/06/2000	FLOW DIFFUSER TO BE USED IN ELECTRO-CHEMICAL DEPOSITION SYSTEM	STEVENS, JOSEPH J
<a href="#">09518182</a>	<a href="#">6432282</a>	150	03/02/2000	METHOD AND APPARATUS FOR SUPPLYING ELECTRICITY UNIFORMLY TO A WORKPIECE	STEVENS, JOSEPH J.
<a href="#">09524977</a>	<a href="#">6517130</a>	150	03/14/2000	SELF POSITIONING VACUUM CHUCK	STEVENS, JOSEPH J.
<a href="#">09603791</a>	<a href="#">6454927</a>	150	06/26/2000	APPARATUS AND METHOD FOR ELECTRO CHEMICAL DEPOSITION	STEVENS, JOSEPH J.
<a href="#">09616308</a>	<a href="#">6612014</a>	150	07/12/2000	DUAL POST CENTRIFUGAL WAFER CLIP FOR SPIN RINSE DRY UNIT	STEVENS, JOSEPH J.
<a href="#">09678947</a>	<a href="#">6582578</a>	150	10/03/2000	METHOD AND ASSOCIATED APPARATUS FOR TILTING A SUBSTRATE UPON ENTRY FOR METAL DEPOSITION	STEVENS, JOSEPH J.

<u>09730968</u>	<u>6613214</u>	150	12/05/2000	ELECTRIC CONTACT ELEMENT FOR ELECTROCHEMICAL DEPOSITION SYSTEM	STEVENS, JOSEPH J.
<u>09731326</u>	<u>6585876</u>	150	12/05/2000	FLOW DIFFUSER TO BE USED IN ELECTRO-CHEMICAL PLATING SYSTEM AND METHOD	STEVENS, JOSEPH J.
<u>09785815</u>	Not Issued	083	02/16/2001	INTEGRATED SEMICONDUCTOR SUBSTRATE BEVEL CLEANING APPARATUS AND METHOD	STEVENS, JOSEPH J.
<u>09981505</u>	<u>6720263</u>	150	10/16/2001	PLANARIZATION OF METAL LAYERS ON A SEMICONDUCTOR WAFER THROUGH NON-CONTACT DE-PLATING AND CONTROL WITH ENDPOINT DETECTION	STEVENS, JOSEPH J.
<u>10036321</u>	<u>6824612</u>	150	12/26/2001	ELECTROLESS PLATING SYSTEM	STEVENS, JOSEPH J.
<u>10039808</u>	Not Issued	161	10/26/2001	METHOD AND APPARATUS FOR SUPPLYING ELECTRICITY UNIFORMLY TO A WORKPIECE	STEVENS, JOSEPH J.
<u>10052015</u>	<u>6742279</u>	150	01/16/2002	APPARATUS AND METHOD FOR RINSING SUBSTRATES	STEVENS, JOSEPH J.
<u>10059572</u>	Not Issued	041	01/28/2002	ELECTROLESS DEPOSITION APPARATUS	STEVENS, JOSEPH J.
<u>10217872</u>	Not Issued	040	08/13/2002	APPARATUS AND METHOD FOR ELECTRO CHEMICAL DEPOSITION	STEVENS, JOSEPH J.
<u>10424479</u>	Not Issued	030	04/28/2003	METHOD AND ASSOCIATED APPARATUS FOR TILTING A SUBSTRATE UPON ENTRY FOR METAL DEPOSITION	STEVENS, JOSEPH J.
<u>10609862</u>	Not Issued	030	06/30/2003	FLOW DIFFUSER TO BE USED IN ELECTRO-CHEMICAL PLATING SYSTEM	STEVENS, JOSEPH J.
<u>11034349</u>	Not Issued	018	01/11/2005	PATTERNED WAFER THICKNESS DETECTION SYSTEM	STEVENS, JOSEPH J.
<u>60216896</u>	Not Issued	159	07/07/2000	FLOW DIFFUSER TO BE USED IN ELECTRO-CHEMICAL DEPOSITION SYSTEM	STEVENS, JOSEPH J.
<u>60237908</u>	Not Issued	159	10/04/2000	INTEGRATED SEMICONDUCTOR SUBSTRATE BEVEL CLEANING APPARATUS AND METHOD	STEVENS, JOSEPH J.
<u>08446396</u>	<u>5632873</u>	150	05/22/1995	TWO PIECE ANTI-STICK CLAMP RING	STEVENS, JOSEPH J.
<u>10777952</u>	Not Issued	030	02/12/2004	METHOD AND CIRCUIT FOR INCREASED NOISE IMMUNITY FOR CLOCKING SIGNALS IN HIGH SPEED DIGITAL SYSTEMS	STEVENS, JOSEPH M.
<u>60262441</u>	Not Issued	159	01/16/2001	UNILINK ANALOG ARCHITECTURE	STEVENS, JOSEPH MARSH
<u>08991145</u>	<u>5990350</u>	150	12/16/1997	PROCESS FOR MAKING GRANULAR L-LYSINE	STEVENS, JOSEPH MICHAEL

<a href="#">09065919</a>	<a href="#">6337084</a>	150	04/24/1998	EXTRUSION OF AMINO ACID ANIMAL FEED SUPPLEMENTS	STEVENS, JOSEPH MICHAEL
<a href="#">09098948</a>	<a href="#">6017555</a>	150	06/17/1998	PROCESS FOR MAKING L-LYSINE FEED SUPPLEMENT	STEVENS, JOSEPH MICHAEL
<a href="#">09694224</a>	<a href="#">6732332</a>	150	10/23/2000	AUTOMATED WEB SITE CREATION SYSTEM	STEVENS, JOSEPH PAUL
<a href="#">60228548</a>	Not Issued	159	08/28/2000	AUTOMATED WEB SITE CREATION SYSTEM	STEVENS, JOSEPH PAUL
<a href="#">60212917</a>	Not Issued	159	08/23/2000	VEHICLE THEFT WARNING AND REMOTE KEYLESS ENTRY SYSTEM	STEVENS, JOSEPH W.
<a href="#">09430145</a>	Not Issued	161	10/29/1999	VEHICLE ALARM NOTIFICATION SYSTEM	STEVENS, JOSEPH WALTER
<a href="#">07290205</a>	<a href="#">4978960</a>	150	12/27/1988	METHOD AND SYSTEM FOR REAL APERTURE RADAR GROUND MAPPING	STEVENSON, JOSEPH
<a href="#">07714135</a>	<a href="#">5164731</a>	159	06/13/1991	TURBULENCE RADAR SYSTEM	STEVENSON, JOSEPH
<a href="#">60270090</a>	Not Issued	159	02/20/2001	HYBRID BRUSHLESS DC MOTOR	STEVENSON, JOSEPH
<a href="#">07480233</a>	Not Issued	161	03/08/1990	VHS VIDEO CASSETTE MAILER BOX	STEVENSON, JOSEPH E
<a href="#">10651015</a>	Not Issued	030	08/26/2003	PERMANENT MAGNET MOTOR	STEVENSON, JOSEPH H
<a href="#">10115194</a>	<a href="#">6832647</a>	150	04/02/2002	INTEGRATED CONDENSER/SEPARATOR FOR FUEL CELL EXHAUST GASES	STEVENSON, JOSEPH R
<a href="#">10115343</a>	Not Issued	094	04/03/2002	CONTACT HEATER/HUMIDIFIER FOR FUEL CELL SYSTEMS	STEVENSON, JOSEPH R
<a href="#">10246223</a>	Not Issued	030	09/18/2002	HUMIDIFICATION OF REACTANT STREAMS IN FUEL CELLS	STEVENSON, JOSEPH R
<a href="#">08631477</a>	<a href="#">5735133</a>	159	04/12/1996	VEHICULAR COOLING SYSTEM WITH THERMAL STORAGE	STEVENSON, JOSEPH R
<a href="#">07613340</a>	<a href="#">5101557</a>	159	11/14/1990	METHOD FOR MACHINING ROTORS	STEVENSON, JOSEPH T
<a href="#">07653218</a>	Not Issued	166	02/11/1991	BLADE DAMPER	STEVENSON, JOSEPH T

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**Inventor Name Search Result**

Your Search was:

Last Name = GARVIN

First Name = STACY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10644374</a>	Not Issued	092	08/20/2003	METHOD AND SYSTEM FOR ADJUSTING A FREQUENCY RANGE OF A DELAY CELL OF A VCO	GARVIN, STACY J.
<a href="#">10650396</a>	Not Issued	041	08/28/2003	SELF-ADAPTIVE VOLTAGE REGULATOR FOR A PHASE-LOCKED LOOP	GARVIN, STACY J.
<a href="#">10685022</a>	Not Issued	071	10/14/2003	CIRCUIT AND METHOD FOR REDUCING JITTER IN A PLL OF HIGH SPEED SERIAL LINKS	GARVIN, STACY J.
<a href="#">06551450</a>	<a href="#">4638464</a>	150	11/14/1983	CHARGE PUMP SYSTEM FOR NON-VOLATILE RAM	GARVIN, STACY J.
<a href="#">06551451</a>	<a href="#">4536720</a>	150	11/14/1983	PROGRAMMABLE OSCILLATOR WITH POWER DOWN FEATURE AND FREQUENCY ADJUSTMENT	GARVIN, STACY J.
<a href="#">06943466</a>	<a href="#">4752699</a>	150	12/19/1986	ON CHMULTIPLE FEEDBACK SENSE CIRCUITS USING A CHARGE PUMP AND PLURAL VOLTAGE GENERATION	GARVIN, STACY J.
<a href="#">08171483</a>	<a href="#">5396449</a>	250	12/21/1993	FAST CONTENT ADDRESSABLE MEMORY WITH REDUCED POWER CONSUMPTION	GARVIN, STACY J.
<a href="#">09004565</a>	<a href="#">6031394</a>	150	01/08/1998	LOW VOLTAGE CMOS CIRCUIT FOR ON/OFF OFF CHIP DRIVE AT HIGH VOLTAGE	GARVIN, STACY J.
<a href="#">09996053</a>	Not Issued	030	11/28/2001	ANALOG UNIDIRECTIONAL SERIAL LINK ARCHITECTURE	GARVIN, STACY JEAN
<a href="#">60262441</a>	Not Issued	159	01/16/2001	UNILINK ANALOG ARCHITECTURE	GARVIN, STACY JEAN

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**Inventor Name Search Result**

Your Search was:

Last Name = NORMAN

First Name = VERNON

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10484608</a>	Not Issued	030	01/20/2004	CLOCK DATA RECOVERING SYSTEM WITH EXTERNAL EARLY/LATE INPUT	NORMAN, VERNON P
<a href="#">10201868</a>	Not Issued	041	07/23/2002	FAST DETECTION OF INCORRECT SAMPLING IN AN OVERSAMPLING CLOCK AND DATA RECOVERY SYSTEM	NORMAN, VERNON R.
<a href="#">10265759</a>	Not Issued	030	10/07/2002	DIGITAL ADAPTIVE CONTROL LOOP FOR DATA DESERIALIZATION	NORMAN, VERNON R.
<a href="#">10265981</a>	Not Issued	030	10/07/2002	METHOD AND SYSTEM FOR DATA AND EDGE DETECTION WITH CORRELATION TABLES	NORMAN, VERNON R.
<a href="#">10375286</a>	Not Issued	030	02/27/2003	RECEIVER FOR CLOCK AND DATA RECOVERY AND METHOD FOR CALIBRATING SAMPLING PHASES IN A RECEIVER FOR CLOCK AND DATA RECOVERY	NORMAN, VERNON R.
<a href="#">10650396</a>	Not Issued	041	08/28/2003	SELF-ADAPTIVE VOLTAGE REGULATOR FOR A PHASE-LOCKED LOOP	NORMAN, VERNON R.
<a href="#">10680679</a>	Not Issued	020	10/07/2003	METHOD AND SYSTEM FOR USING STATISTICAL SIGNATURES FOR TESTING HIGH-SPEED CIRCUITS	NORMAN, VERNON R.
<a href="#">10685022</a>	Not Issued	071	10/14/2003	CIRCUIT AND METHOD FOR REDUCING JITTER IN A PLL OF HIGH SPEED SERIAL LINKS	NORMAN, VERNON R.
<a href="#">10709805</a>	Not Issued	030	05/28/2004	METHOD AND SYSTEM FOR PROVIDING QUALITY CONTROL ON WAFERS RUNNING ON A MANUFACTURING LINE	NORMAN, VERNON R.
<a href="#">10720974</a>	Not Issued	030	11/24/2003	METHOD FOR DETERMINING JITTER OF A SIGNAL IN A SERIAL LINK AND HIGH SPEED SERIAL LINK	NORMAN, VERNON R.
<a href="#">10791175</a>	Not Issued	020	03/02/2004	CIRCUIT AND METHOD FOR PROVIDING AUTOMATIC ADAPTATION TO FREQUENCY OFFSETS IN HIGH SPEED SERIAL LINKS	NORMAN, VERNON R.
<a href="#">06810139</a>	<a href="#">4748588</a>	150	12/18/1985	FAST DATA SYNCHRONIZER	NORMAN, VERNON R.

<u>07368739</u>	<u>4932023</u>	250	06/20/1989	FRAME STRIPPING PROTOCOL FOR EARLY TOKEN RELEASE IN A RING COMMUNICATION NETWORK	NORMAN, VERNON R.
<u>07422168</u>	<u>5142530</u>	150	10/16/1989	MULTI-FRAME STRIPPING PROTOCOL FOR TOKEN RING NETWORKS	NORMAN, VERNON R.
<u>08024593</u>	<u>5537623</u>	150	03/01/1993	MULTIPLE GROUP ADDRESS RECOGNITION	NORMAN, VERNON R.
<u>08200989</u>	<u>5442629</u>	150	02/24/1994	TOKEN RING SPEED DETECTOR	NORMAN, VERNON R.
<u>08203239</u>	Not Issued	166	02/28/1994	SEGMENTED COMMUNICATIONS ADAPTER WITH PACKET TRANSFER INTERFACE	NORMAN, VERNON R.
<u>08513711</u>	<u>5719864</u>	150	08/11/1995	LOGICAL CHANNEL RESOLUTION IN ASYNCHRONOUS TRANSMISSION MODE (ATM) COMMUNICATION SYSTEMS	NORMAN, VERNON R.
<u>08604201</u>	<u>5717855</u>	150	02/21/1996	SEGMENTED COMMUNICATIONS ADAPTER WITH PACKET TRANSFER INTERFACE	NORMAN, VERNON R.
<u>09996053</u>	Not Issued	030	11/28/2001	ANALOG UNIDIRECTIONAL SERIAL LINK ARCHITECTURE	NORMAN, VERNON ROBERTS
<u>09996091</u>	Not Issued	121	11/28/2001	ARCHITECTURE FOR ADVANCED SERIAL LINK BETWEEN TWO CARDS	NORMAN, VERNON ROBERTS
<u>09996113</u>	Not Issued	030	11/28/2001	UNIFIED DIGITAL ARCHITECTURE	NORMAN, VERNON ROBERTS
<u>09997587</u>	Not Issued	041	11/28/2001	APPARATUS AND METHOD FOR OVERSAMPLING WITH EVENLY SPACED SAMPLES	NORMAN, VERNON ROBERTS
<u>10915790</u>	Not Issued	020	08/11/2004	METHODS AND ARRANGEMENTS FOR LINK POWER REDUCTION	NORMAN, VERNON ROBERTS
<u>60262358</u>	Not Issued	150	01/16/2001	GLOBAL ARCHITECTURE FOR ADVANCED SERIAL LINK	NORMAN, VERNON ROBERTS
<u>60262401</u>	Not Issued	150	01/16/2001	UNILINK DIGITAL ARCHITECTURE	NORMAN, VERNON ROBERTS
<u>60262441</u>	Not Issued	150	01/16/2001	UNILINK ANALOG ARCHITECTURE	NORMAN, VERNON ROBERTS
<u>60266365</u>	Not Issued	159	02/02/2001	APPARATUS AND METHOD FOR OVERSAMPLING WITH EVENLY SPACED SAMPLES	NORMAN, VERNON ROBERTS

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**Inventor Name Search Result**

Your Search was:

Last Name = OWCZARSKI

First Name = PAUL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10266132</a>	Not Issued	041	10/07/2002	METHOD AND SYSTEM FOR TERMINATION OF TRANSMISSION CHANNELS IN MODULAR CIRCUITS	OWCZARSKI, PAUL A.
<a href="#">09225111</a>	<a href="#">6298458</a>	150	01/04/1999	SYSTEM AND METHOD FOR MANUFACTURING TEST OF A PHYSICAL LAYER TRANSCEIVER	OWCZARSKI, PAUL A.
<a href="#">09996053</a>	Not Issued	030	11/28/2001	ANALOG UNIDIRECTIONAL SERIAL LINK ARCHITECTURE	OWCZARSKI, PAUL ALAN
<a href="#">60262441</a>	Not Issued	159	01/16/2001	UNILINK ANALOG ARCHITECTURE	OWCZARSKI, PAUL ALAN

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**Inventor Name Search Result**

Your Search was:

Last Name = CRANFORD

First Name = HAYDEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">60112483</a>	Not Issued	159	12/16/1998	METHOD AND APPARATUS FOR TRANSMITTING AND RECEIVING ETHERNET OVER SHIELDED TWISTED PAIR (STP) CABLING SYSTEM	CRANFORD JR., HAYDEN C.
<a href="#">09023020</a>	<a href="#">6087861</a>	150	02/11/1998	WAVEFORM GENERATORS INCLUDING BALANCED CURRENT SUPPLIES AND RELATED METHODS	CRANFORD JR., HAYDEN CLAVIE
<a href="#">09338214</a>	<a href="#">6275094</a>	150	06/22/1999	CMOS DEVICE AND CIRCUIT AND METHOD OF OPERATION DYNAMICALLY CONTROLLING THRESHOLD VOLTAGE	CRANFORD JR., HAYDEN CLAVIE
<a href="#">09160802</a>	<a href="#">6249164</a>	150	09/25/1998	DELAY CIRCUIT ARRANGEMENT FOR USE IN A DAC/DRIVER WAVEFORM GENERATOR WITH PHASE LOCK RISE TIME CONTROL	CRANFORD JR., HAYDEN CLAY
<a href="#">09160907</a>	<a href="#">6721379</a>	150	09/25/1998	DAC/DRIVER WAVEFORM GENERATOR WITH PHASE LOCK RISE TIME CONTROL	CRANFORD JR., HAYDEN CLAY
<a href="#">10484608</a>	Not Issued	030	01/20/2004	CLOCK DATA RECOVERING SYSTEM WITH EXTERNAL EARLY/LATE INPUT	CRANFORD, HAYDEN C
<a href="#">09749908</a>	Not Issued	092	12/29/2000	PROGRAMMABLE DRIVER/EQUALIZER WITH ALTERABLE ANALOG FINITE IMPULSE RESPONSE (FIR) FILTER HAVING LOW INTERSYMBOL INTERFERENCE & CONSTANT PEAK AMPLITUDE INDEPENDENT OF COEFFICIENT SETTINGS	CRANFORD, HAYDEN C
<a href="#">10014455</a>	Not Issued	030	12/14/2001	DYNAMIC MEASUREMENT OF COMMUNICATION CHANNEL CHARACTERISTICS USING DIRECT SEQUENCE SPREAD SPECTRUM (DSSS) SYSTEMS, METHODS AND PROGRAM PRODUCTS	CRANFORD, HAYDEN C
<a href="#">10065475</a>	Not Issued	041	10/22/2002	TERMINATING RESISTOR DRIVER FOR HIGH SPEED DATA COMMUNICATION	CRANFORD, HAYDEN C
<a href="#">10265755</a>	Not	030	10/07/2002	METHOD AND SYSTEM FOR	CRANFORD, HAYDEN C

	Issued			SCALABLE PRE-DRIVER TO DRIVER INTERFACE	
<u>10265759</u>	Not Issued	030	10/07/2002	DIGITAL ADAPTIVE CONTROL LOOP FOR DATA DESERIALIZATION	CRANFORD, HAYDEN C
<u>10265981</u>	Not Issued	030	10/07/2002	METHOD AND SYSTEM FOR DATA AND EDGE DETECTION WITH CORRELATION TABLES	CRANFORD, HAYDEN C
<u>10266132</u>	Not Issued	041	10/07/2002	METHOD AND SYSTEM FOR TERMINATION OF TRANSMISSION CHANNELS IN MODULAR CIRCUITS	CRANFORD, HAYDEN C
<u>10319705</u>	Not Issued	030	12/13/2002	SYSTEM AND METHOD FOR TRANSMITTING DATA AND ADDITIONAL INFORMATION SIMULTANEOUSLY WITHIN A WIRE BASED COMMUNICATION SYSTEM	CRANFORD, HAYDEN C
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<u>10791175</u>	Not Issued	020	03/02/2004	CIRCUIT AND METHOD FOR PROVIDING AUTOMATIC ADAPTATION TO FREQUENCY OFFSETS IN HIGH SPEED SERIAL LINKS	CRANFORD, HAYDEN C
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<u>10906628</u>	Not Issued	030	02/28/2005	BODY-BIASED ENHANCED PRECISION CURRENT MIRROR	CRANFORD, HAYDEN C
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<u>06943466</u>	<u>4752699</u>	150	12/19/1986	ON CHMULTIPLE FEEDBACK SENSE CIRCUITS USING A CHARGE PUMP AND PLURAL VOLTAGE GENERATION	CRANFORD, HAYDEN C
<u>08632976</u>	<u>5731737</u>	150	04/16/1996	METHOD AND APPARATUS FOR REDUCING CLOCK SWITCHING NOISE IN CONTINUOUS TIME FILTERS	CRANFORD, HAYDEN C
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<u>09225111</u>	<u>6298458</u>	150	01/04/1999	SYSTEM AND METHOD FOR MANUFACTURING TEST OF A PHYSICAL LAYER TRANSCEIVER	CRANFORD, HAYDEN C
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<u>10769437</u>	Not Issued	030	01/30/2004	PHASE ROTATOR, PHASE ROTATION METHOD AND CLOCK AND DATA RECOVERY RECEIVER INCORPORATING SAID PHASE ROTATOR	CRANFORD, HAYDEN CLAVIE
<u>10873672</u>	Not Issued	030	06/22/2004	REDUCING POWER CONSUMPTION IN SIGNAL DETECTION	CRANFORD, HAYDEN CLAVIE
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<a href="#">08910165</a>	<a href="#">5872446</a>	150	08/12/1997	LOW VOLTAGE CMOS ANALOG MULTIPLIER WITH EXTENDED INPUT DYNAMIC RANGE	CRANFORD, HAYDEN CLAVIE
<a href="#">08910956</a>	<a href="#">5942999</a>	250	08/08/1997	CONTROLLABLE INTEGRATED LINEAR ATTENUATOR FOR A D/A CONVERTER	CRANFORD, HAYDEN CLAVIE

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